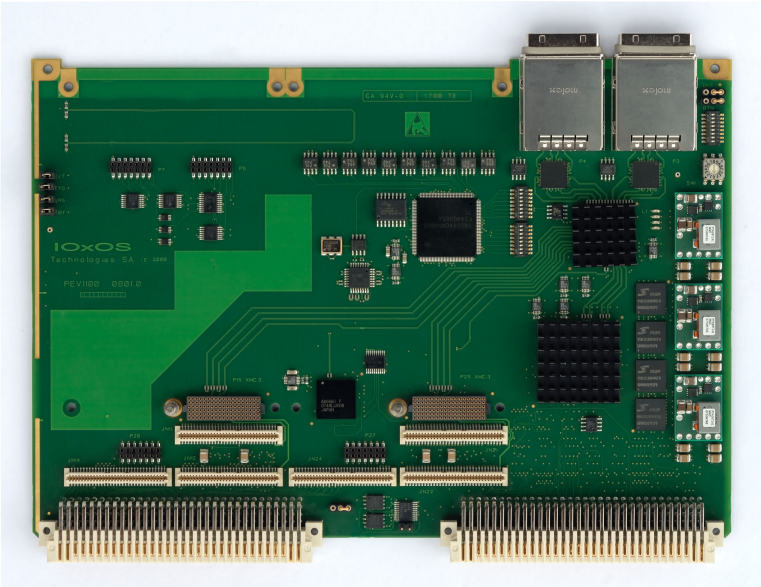
IOxOS Technologies announces the first PCI Express to VME64x direct Interface for high performance customized applications.

Gland, Switzerland, May 2008 - -IOxOS technologies unveils its **PEV 1100**, innovative high performance PCI Express to VME64x interconnecting solution for upgrading existing VME based systems to PCI Express technology. Combining PCI Express innovation with VME IO legacy compatibility, PEV1100 is the suited product for looking at future while preserving existing hardware and software investment. In fact, the PEV 1100 allows the user to remove the computing power from the VME crate and benefit from cheap and high performance COTS hardware without trowing away valuable I/O boards

By featuring two front panel PCI Express External cabling connectors, PEV 1100 creates new opportunities for building multi-chassis and dual host topologies. An upstream port allows transparent data transfers between a local host and IO resources housed in VME chassis, by extending the host Computer PCI Express bus up to 7 meters. A second port can connect either a system host or another PEV1100 for multi-chassis configurations. For long distances up to 100 meter optical connection is provided. Cables and connectors are not proprietary but fully specified by PCI Express External Cabling 1.0 specification and largely available on the market.

The **PCI Express x4 to VME 64x/2eSST** direct bridge is the key feature, which makes PEV 1100 an attractive and unique interconnect solution. The transparent PCI Express to VME64x interface, not making use of intermediate bridging, minimizes dead time and optimizes latency. Interconnecting directly the host computer to the VME bus, PEV1100 maintains a single global memory mapped address space without requiring any software protocol support. This powerful capability will enable a widespread adoption of PEV 1100 in conjunction with latest generation host computers.



The VME64x unit implements full VME64x Master/Slave interface with Slot 1 function and handles all data transfer types (BLT, MBLT, 2eVME and 2eSST). In order to benefit from the full band width of PCI Express PEV 1100 is equipped with a 4-channel Intelligent DMA with chaining capability and a 256MB Shared Memory, embedded into the VME64x interface.

Built around latest FPGA generation, PEV 1100 offers to developers a User Area dedicated to custom applications. A 4 ports central switch provides high speed path among User Area, PCI Express bus, VME64x bus and Shared Memory. Two on board mezzanine PMC/XMC 42.3 sites with direct path to the User Area, allows local IO or RT computing extension, enhancing the PEV 1100 flexibility to handle a large range of applications

The product range offers VME64x 5-row connectors or legacy VME 3-row connectors versions.

API and Devices Driver software support for LINUX and Windows are provided with a Starter Kit , including PEV3100 – the PCI Express Host adapter - and 3 meter copper cable as well as three month phone support.

PEV 1100 will be available for shipping in quantity third quarter 2008.

For pricing and ordering information, please visit **http://www.ioxos.ch**

IOxOS technologies SA, based in the Geneva area in Switzerland, is an electronic design company offering innovative solutions to system integrators in the aerospace, physics and telecomunication industry. It combines a comprehensive product line with engineering, consulting and training services covering both hardware and software.